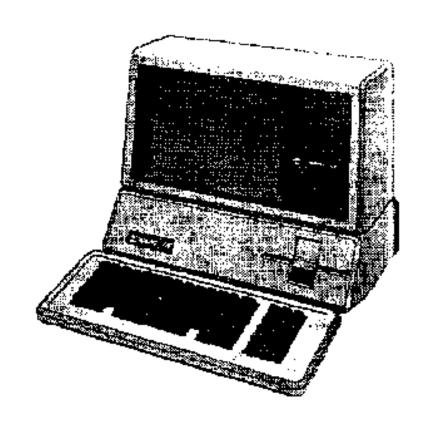


## **▲** Apple /// Computer Technical Information

Apple ///
Serial Printer Port
Driver 1.30
Source Code Listing



Created by David T. Craig 07 January 1998 • 71533.606@compuserve.com



## FORMATTED LISTING

```
PROJECT : Apple /// SOS Serial Printer Driver 1.30 (6502 Assembly Source Code)
FILE NAME: SERPRINT.text
000001
                                                                .TITLE
                                                                                                  "SOS Serial Printer Driver"
000002
                                                                .NOPATCHLIST
000003
                                                                .NOMACROLIST
000004
000005
000006
                                                              SOS Serial Printer Driver
000007
000008
                                                              Copyright (C) 1983 by Apple Computer Inc.
000010
                                                              All Rights Reserved
000011
000011
000013
                                         Revisions:
000014
000015
                                          1.00
                                                             14-Nov-80
000016
000017
                                         1.10
                                                              14-Apr-81
000018
                                                              Bug fixes:
000019
                                                               Switch to 1 MHz for all ACIA references.
000020
                                                               Check buffer count and delay count for write completion.
000021
000022
                                          1.30
                                                              Bug fixes:
Add XMIT flag for improved communications between Driver
000023
000024
000025
                                                                      and Interrupt Handler.
000026
000027
000028
000029
                    DEVTYPE
                                                               . EOU
                                                                                                  41
000030
                     SUBTYPE
                                                                                                  01
                                                               . EQU
                    APPLE
000031
000032
                                                                .EQU
                                                                                                 0001
                    RELEASE
                                                                . EOU
                                                                                                 1300
000033
000034
000035
000036
                            The macro SWITCH performs an N way branch based on a switch index.
                            maximum value of the switch index is 127 with bounds checking provided as an option. The macro uses the A and Y registers and alters the C,
000037
000038
000039
                            {\tt Z}, and {\tt N} flags of the status register, but the X register is unchanged.
000040
000041
                                                               SWITCH [index], [bounds], adrs_table, [*]
000042
000043
                                         index
                                                              This is the variable that is to be used as the switch index. If omitted, the value in the accumulator is used.
000044
000045
                                                              This is the maximum allowable value for index. If index exceeds this value, the carry bit will be set and execution will continue following the macro. If bounds is omitted,  \frac{1}{2} \int_{-\infty}^{\infty} \frac{1}{2} \left( \frac{1}{2} \int_
000046
                                       bounds
000047
000048
000049
                                                              no bounds checking will be performed.
000050
000051
                            adrs_table
                                                              This is a table of addresses (low byte first) used by the
000052
                                                              switch. The first entry corresponds to index zero.
000053
000054
                                                              If an asterisk is supplied as the fourth parameter, the
                                                              macro will push the switch address but will not exit to
it; execution will continue following the macro. The
000055
000056
000057
                                                              program may then load registers or set the status before
000058
                                                               exiting to the switch address.
000059
000060
000061
000062
                                                               .MACRO
                                                                                                  SWITCH
                                                                                                  "%1" <> ""
                                                                                                                                                                     ;If PARM1 is present, ; Load A with switch index
000063
                                                                TF
000064
                                                                                                  %1
                                                               LDA
000065
                                                               . ENDC
                                                                                                  "%2" <> ""
                                                                                                                                                                      ;If PARM2 is present,
000066
                                                                TF
                                                                                                                                                                     ; Perform bounds checking
; on switch index
000067
                                                               CMP
                                                                                                 #%2+1
000068
                                                               BCS
                                                                                                  $3579
000069
                                                                . ENDC
000070
                                                               ASL
                                                                                                 Α
000071
                                                               TAY
000072
                                                                                                  %3+1,Y
                                                                                                                                                                      ;Get switch address from table
                                                               LDA
000073
                                                               PHA
                                                                                                                                                                      ; and push onto stack
000074
                                                               T.DA
                                                                                                  %3,Y
000075
                                                               PHA
                                                                                                  "%4" <> "*"
000076
                                                               .IF
                                                                                                                                                                      ; If PARM4 is omitted,
000077
                                                               RTS
                                                                                                                                                                           Exit to code
000078
                                                                . ENDC
                                                                                                                                                                      ;Otherwise, drop through
000079
                    $3579
                                                                .ENDM
000080
                                                                . PROC
                                                                                                 SERPRNT
000081
                                                                .WORD
                                                                                                  OFFFF
                                                                .WORD
                                                                                                   "Serial Printer Driver -- "
000083
                                                                . ASCIT
000084
                                                                .ASCII
                                                                                                  "Copyright (C) 1983 by Apple Computer Inc."
```



```
000085
000086
000087
000088
            Device Handler Identification Block
000089
000090
000091
                                         0000
                                                                      ;Link to next device handler ;Entry point address
000092
         IDBLK
                           . WORD
000093
                          .WORD
                                         SP_MAIN
000094
                           .BYTE
                                         8
".PRINTER
                                                                      ;Length of device name
000095
                           . ASCIT
000096
                                         80,00,00
                           .BYTE
                                                                      ;Device, Slot & Unit numbers
000097
                           .BYTE
                                         DEVTYPE
000098
                           BYTE
                                         SUBTYPE
000099
                           BYTE
                                         0000
000100
                           .WORD
000101
                           .WORD
                                         APPLE
000102
                           .WORD
                                         RELEASE
000103
000104
000105
000106
000107
           Device Handler Configuration Block
000108
000109
000110
000111
                           .WORD
                                         0.5
                                                                      ;Configuration block length
000112
         DRATE
                           BYTE
                                         0.8
                                                                      ;Data Rate
000113
         DFORMAT
                           .BYTE
                                                                      ;Data Format
000114
         CRDELAY
                           .BYTE
                                         00
                                                                      ;Carriage return delay
000115
         LFDELAY
                           .BYTE
                                         00
                                                                      ;Line feed delay
000116
         FFDELAY
                           .BYTE
                                                                      ;Form feed delay
000117
                           .PAGE
000118
000119
000120
000121
         ; SOS Global Data & Subroutines
000122
000123
000124
         ALLOCSIR
                          .EOU
                                        1913
000125
         DEALCSIR
                          .EQU
000126
         SYSERR
                          .EOU
                                       1928
000127
000128
000129
000130
000131
         ; SOS Error Codes
000132
000133
000134
                                         20
         XREOCODE
                           . EOU
                                                                      ;Invalid request code
000135
         XCTLCODE
                                                                      ;Invalid control/status code
000136
                          . EQU
                                         21
000137
         XNOTOPEN
                          .EQU
                                                                      ;Device not open
                                                                      Device not available
000138
         XNOTAVII.
                           .EOU
                                         24
000139
         XNORESRC
                          .EQU
                                                                      ;Resource not available
000140
         XBADOP
                           . EQU
                                                                      ;Invalid operation for device
000141
000142
000143
000144
         ; Hardware I/O Addresses
000145
000146
000147
000148
000149
000150
         ACIADATA
                           .EQU
                                         0C0F0
                                                                      ;ACIA data register
                                         0C0F1
         ACIASTAT
                                                                      ;ACIA status register
                           .EOU
000151
         ACIACMD
                          . EQU
                                         0C0F2
                                                                      ;ACIA command register
000152
         ACIACTL
                          . EQU
                                         0C0F3
0FFDF
                                                                      ;ACIA control register
;Environment register
000153
         E_REG
000154
000155
000156
000157
000158
000159
         ; Miscellaneous Equates
000160
000161
000162
000163
        TRUE
                           .EQU
                                        80
        FALSE
ASC_LF
000164
                           .EOU
                                         0.0
000165
                           . EQU
000166
000167
        ASC_FF
ASC_CR
                                         0C
0D
                           .EQU
                           . EOU
         BITON4
                          . EQU
000169
000170
         BITON7
                           .EQU
                           . PAGE
000171
000172
000173
         ; SOS Device Handler Interface
000174
000175
000176
000177
         SOSINT
                          .EQU
                                        0C0
```



```
000178 REQCODE
                           .EQU
                                          SOSINT+0
                                                                        ;SOS request code
000179
000180
        BUFFER
REQCNT
                           . EQU
                                          SOSTNT+2
                                                                        ;Buffer pointer ;Requested count
                                          SOSINT+4
000181
         CTLSTAT
                                          SOSINT+2
                                                                        ;Control/status code
000182
000183
         CSLIST
                           . EOU
                                          SOSINT+3
                                                                        ;Control/status list pointer
000184
000185
000186
000187
         ; Zero Page Storage
000188
000189
000190
         ZPGSAVE
000191
                           . EOU
                                         SOSINT+0A
                                                                       ;Saved zero page storage
000192
                           .EQU
000193
         ZPGTEMP
                                         ZPGSAVE+00
                                                                       ;Temporary zero page storage
000194
         MOVCNT
                                         ZPGTEMP+00
                           .EOU
000195
000196
000197
000198
000199
             Private Variable Storage
000200
000201
000202
000203
         SIRADDR
                           .WORD
                                          SIRTABLE
000204
         SIRTABLE
                           .BYTE
                                                                        ;ACIA resource
                                          ACIAMIH
000205
                           WORD
000206
                           .BYTE
         MIHBANK
000207
         SIRCOUNT
                                          *-SIRTABLE
         OPENFLG
000208
                           .BYTE
                                          FALSE
                                                                       ;Device open flag
;XMIT in progress flag
000209
         TIMX
                           .BYTE
                                          FALSE
000210
000211
         DLYCNT
                           .BYTE
                                                                       ;Delay count for MIH
;Local buffer byte count
         BUFCNT
                                          0
                           .BYTE
000212
                                                                        ;Local buffer head index
         BUFHEAD
                           .BYTE
000213
000214
         BUFTAIL
                           .BYTE
                                          Λ
                                                                        ;Local buffer tail index
                                          110.
                                                                        ;Local buffer size
         BUFSIZE
                           .EOU
000215
         LOCBUF
                           .EQU
                                                                        ;Local buffer
                                          "Copyright (C) 1983 by Apple Computer Inc." *-LOCBUF
000216
                            . ASCIT
000217
         CPYRGHTSIZ
                           . EQU
                                          BUFSIZE-CPYRGHTSIZ,0
000218
                           .BLOCK
000219
                           .PAGE
000220
000221
000222
            Serial Printer Driver -- Main entry point
000223
000224
000225
000226
        SP_MAIN
000227
                           SWITCH
                                         REQCODE, 8, SP_REQSW
000228
000229
000230
         BADREQ
                           LDA
                                          #XREQCODE
                                                                       ;Invalid request code
000231
                           JSR
                                          SYSERR
000232
000233
                                          #XNOTOPEN
000234
        NOTOPEN
                           LDA
                                                                       Device not open
000235
000236
000237
                                                                        ;Serial Printer request switch
000238
        SP_REQSW
000239
000240
                           .WORD
                                          SP_READ-1
SP_WRITE-1
000241
                           .WORD
                                          SP_STAT-1
000242
000243
                           . WORD
                                          SP CNTL-1
                           . WORD
                                          BADREQ-1
000244
                           .WORD
                                          BADREQ-1
000245
                           . WORD
                                          SP_OPEN-1
SP_CLOSE-1
000246
                           .WORD
000247
                           .WORD
                                          SP_INIT-1
000248
                           . PAGE
000249
000250
000251
            Serial Printer Driver -- Initialization Request
000252
000253
000254
000255
         SP_INIT
                           .EQU
000256
000257
                           LDÃ
                                          #FALSE
                           STA
                                          OPENFLG
                                                                        ;Validate data rate
                                          DRATE
000259
000260
                           AND
                                          #00F
                                          DRATE
                           STA
000262
000263
                                          DFORMAT
                           T.DA
                                                                       ;Validate data format
                           AND
                                          #0EE
000264
                           ORA
000265
000266
                           CPX
                                          #03
                                                                       ;If data rate is 110 baud
                                          $010
                           BNE
000267
                           ORA
                                          #080
                                                                        ; force two stop bits
         $010
                                          DFORMAT
000268
                           STA
000269
                           CLC
000270
```



```
000271
                           .PAGE
000272
000272
000274
            Serial Printer Driver -- Open Request
000275
000276
000277
000278
         SP OPEN
                           . FOU
000279
                                         OPENFLG
                           BIT
                                                                      ;Serial Printer open?
000280
                          BPL
                                         $010
                                                                      ; No
000281
                                         #XNOTAVIL
                          T<sub>1</sub>DA
000282
                                         SYSERR
000283
000284
         $010
                          T.DA
                                         B REG
000285
                          AND
000286
000287
                                         MIHBANK
                          STA
                                                                      ;Set interrupt handler bank
                                         #SIRCOUNT
                          T<sub>1</sub>DA
000288
                                         SIRADDR
000289
000290
                          LDY
                                         SIRADDR+1
ALLOCSIR
                          JSR
                                                                      ;Allocate the ACIA
000291
                          BCS
                                         $020
000292
000293
                          LDA
                                         #FALSE
                                         XMIT
CNTL00
000294
                           STA
000295
                          JTSR
                                                                      ;Set up ACIA
000296
                          LDA
                                         #TRUE
000297
                           STA
                                         OPENFLG
                                                                      ;Set serial printer open
000298
                          RTS
000299
                          LDA
000300
         $020
                                         #XNORESRC
000301
                          JSR
                                         SYSERR
000302
                           .PAGE
000303
000304
000305
           Serial Printer Driver -- Close Request
000306
000307
000308
000309
         SP_CLOSE
                           .EOU
000310
                                         OPENFLG
                                                                      ;Serial Printer open?
                          ASL
000311
                                         $010
                                                                      ; Yes
                                         NOTOPEN
000312
                          JMP
000313
000314
         $010
                                                                      ;Wait for write completion
000315
                          BMT
                                         $010
000316
                          PHP
000317
                                         E REG
000318
                          T<sub>1</sub>DA
000319
                          TAX
000320
                           ORA
                                         #BITON7
                                         E_REG
ACIASTAT
                                                                      ;Switch to 1 MHz
000321
                          STA
000322
                          STA
                                                                      Reset the ACIA
000323
                           STX
                                         E_REG
000324
                          PLP
000325
                          LDA
                                         #SIRCOUNT
000326
                          LDX
                                         SIRADDR
SIRADDR+1
000327
                          LDY
000328
                                                                      ;Deallocate the ACIA
000329
000330
                          RTS
                           .PAGE
000331
000332
000333
            Serial Printer Driver -- Read Request
000334
000335
000336
000337
000338
                          BIT
BMI
                                         OPENFLG
                                                                      ;Serial Printer open?
000339
                                         $010
000340
                          JMP
                                         NOTOPEN
000341
         $010
                          LDA
                                         #XBADOP
000342
                          JSR
                                         SYSERR
000343
                          .PAGE
000344
000345
000346
         ; Serial Printer Driver -- Write Request
000347
000348
000349
000350
         SP WRITE
                          .EQU
BIT
                                         OPENFLG
                                         $010
NOTOPEN
000352
                          RMT
000353
                          JMP
                                         #BUFSIZE/2
                                                                      ;Set MOVCNT to the lesser
                                         REQCNT+1
$020
                                                                      ; of BUFSIZE/2 and REQCNT.
000355
                          LDY
000356
                          BNE
000357
                           CMP
                                         REQCNT
000358
                          BCC
                                         $020
000359
                          LDA
                                         REOCNT
000360
                          BNE
                                         $020
000361
                          RTS
                                                                      ;Count = zero -- all done!
000362
                                         MOVCNT
         $020
                          STA
000363
```



000001				
000364		LDA	BUFFER+1	Check for buffer
000365		CMP	#OFF	; address overflow
000366		BCC	\$030	
000367		SBC	#080	
000368		STA	BUFFER+1	
000369		INC	1401+BUFFER	
000370	4020	ana.		
000371	\$030	SEC	#DITECT CE	
000372		LDA	#BUFSIZE	
000373 000374	\$040	SBC	MOVCNT	;Wait for room in buffer
000374	\$040	CMP BCC	BUFCNT \$040	, wait for room in buffer
000375		DCC	Q-10	
000377		LDY	#0	
000377		LDX	BUFTAIL	
000379	\$050	LDA	(BUFFER),Y	;Move data to local buffer
000380		STA	LOCBUF,X	
000381		INX		
000382		CPX	#BUFSIZE	
000383		BCC	\$060	
000384		LDX	#0	
000385	\$060	INY		
000386		CPY	MOVCNT	
000387		BCC	\$050	
000388 000389		STX	BUFTAIL	
000369		PHP		
000390		SEI		;Shut down interrupts
000392		CLC		/blide dowlf illectraped
000393		LDA	BUFCNT	
000394		ADC	MOVCNT	Bump buffer count
000395		STA	BUFCNT	-
000396				
000397		BIT	XMIT	;Already transmitting?
000398		BVS	\$070	; Yes
000399		LDA	#0C0	
000400		STA	XMIT	;Set transmitting flag
000401		LDA	E_REG	
000402		PHA	#DIMON7	· Conit alo tra 1 MIII-
000403 000404		ORA STA	#BITON7 E_REG	;Switch to 1 MHz
000404		LDY	ACIASTAT	;Fake an interrupt to start
000406		JSR	ACIAMIH	; the interrupt handler.
000407		PLA	11011111111	, one interrupe nanarer.
000408		STA	E_REG	;Switch back to 2 MHz
000409	\$070	PLP		
000410				
000411		CLC		
000412		LDA	BUFFER	
000413		ADC	MOVCNT	;Fix up buffer pointer
000414		STA	BUFFER	
000415		BCC	\$080	
000416		INC	BUFFER+1	
000417	±000	SEC		
000417		SEC		
000418	\$080	T.DA	PEOCNT	
000418 000419	\$080	LDA SBC	REQCNT MOVENT	Fix up requested count
000418 000419 000420	\$080	SBC	MOVCNT	Fix up requested count
000418 000419 000420 000421	\$080	SBC STA	MOVCNT REQCNT	;Fix up requested count
000418 000419 000420	\$080	SBC	MOVCNT	Fix up requested count;
000418 000419 000420 000421 000422	\$080	SBC STA BCS	MOVCNT REQCNT \$010	Fix up requested count;
000418 000419 000420 000421 000422 000423		SBC STA BCS DEC JMP . PAGE	MOVENT REQUIT \$010 REQUIT+1 \$010	;Loop back for more
000418 000419 000420 000421 000422 000423 000424 000425 000426	;	SBC STA BCS DEC JMP . PAGE	MOVENT REQUIT \$010 REQUIT+1 \$010	
000418 000419 000420 000421 000422 000423 000424 000425 000427	;;	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 REQCNT+1 \$010	;Loop back for more
000418 000419 000420 000421 000422 000423 000424 000425 000426 000427 000428	;; ; ; ACIA Maste	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 REQCNT+1 \$010	;Loop back for more
000418 000419 000420 000421 000422 000423 000424 000425 000426 000427 000428 000429	;; ; ACIA Maste	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 REQCNT+1 \$010 	;Loop back for more
000418 000419 000420 000421 000422 000423 000425 000426 000427 000428 000429 000430	;; ; ACIA Maste	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 REQCNT+1 \$010 	;Loop back for more
000418 000419 000420 000421 000422 000423 000424 000425 000427 000428 000429 000430 000431	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 REQCNT+1 \$010 	;Loop back for more
000418 000419 000420 000421 000422 000423 000424 000425 000427 000428 000429 000430 000431	;; ; ACIA Maste	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler	;Loop back for more
000418 000419 000420 000421 000422 000423 000425 000426 000427 000428 000429 000431 000431 000432	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE :r Interrupt :	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG	;Loop back for more
000418 000419 000420 000421 000422 000423 000425 000426 000427 000428 000429 000431 000431	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE	MOVCNT REQCNT \$010 RECCNT+1 \$010  Handler  * E_REG #BITON7	;Loop back for more
000418 000419 000420 000421 000423 000424 000425 000426 000427 000428 000430 000431 000433 000433	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE er Interrupt :	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG	;Loop back for more
000418 000419 000420 000421 000422 000423 000425 000426 000427 000428 000429 000431 000432 000433 000434 000434	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE er Interrupt :	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status
000418 000419 000420 000421 000422 000423 000425 000426 000427 000428 000431 000433 000433 000435 000436 000437	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE .FAGE .EQU LDA ORA STA TYA AND	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG #60	;Loop back for more
000418 000419 000420 000421 000423 000424 000425 000427 000428 000431 000431 000433 000434 000435 000437	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA TYA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status
000418 000419 000420 000421 000423 000424 000425 000426 000427 000430 000431 000431 000433 000434 000437 000438	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA TYA AND BNE	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG #60	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake
000418 000419 000420 000421 000423 000424 000425 000426 000427 000430 000431 000432 000433 000435 000436 000437 000438 000440 000441	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE TYA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG #60 \$080	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register
000418 000419 000420 000421 000423 000424 000425 000427 000428 000431 000433 000433 000434 000435 000436 000437 000438	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE OF Interrupt .EQU LDA ORA STA TYA AND BNE TYA AND	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG #60 \$080	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake
000418 000419 000420 000421 000423 000424 000425 000426 000427 000430 000431 000431 000433 000434 000435 000439 000440 000441	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE TYA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG #60 \$080	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register
000418 000419 000420 000421 000423 000424 000425 000426 000427 000438 000431 000433 000433 000436 000437 000438 000439 000441 000442	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit
000418 000419 000420 000421 000423 000424 000425 000427 000428 000431 000431 000432 000433 000434 000435 000439 000441 000444	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE TINTETRUPT  .EQU LDA ORA STA TYA AND BNE TYA AND BEQ LDA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress?
000418 000419 000420 000421 000423 000424 000425 000426 000427 000438 000431 000433 000433 000436 000437 000438 000439 000441 000442	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit
000418 000419 000420 000421 000423 000424 000425 000426 000427 000430 000431 000431 000433 000433 000434 000435 000433 000444 000444 000444 000444	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE TINTETRUPT .EQU LDA ORA STA TYA AND BNE TYA AND BNE TYA AND BEQ LDA BEQ LDA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress?
000418 000419 000420 000421 000423 000425 000426 000427 000428 000431 000431 000433 000434 000435 000433 000434 000435 000434 000445 000441 000442 000444 000445 000444	;; ; ; ACIA Maste ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ LDA BEQ DEC	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010 DLYCNT	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress?
000418 000419 000420 000421 000423 000425 000426 000427 000428 000431 000431 000433 000434 000435 000434 000435 000441 000442 000444 000441	;; ; ACIA Maste ; ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ LDA BEQ DEC JMP LDA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010 DLYCNT \$060  BUFCNT	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress? ; no  ;Any data to transmit?
000418 000419 000420 000421 000423 000424 000425 000427 000428 000431 000433 000433 000434 000435 000436 000437 000444 000445 000444 000445 000446 000447	;; ; ACIA Maste ; ;	SBC STA BCS DEC JMP .PAGE TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ LDA BEQ DEC JMP  LDA BEQ LDA BEQ DEC JMP	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010  DLYCNT \$010  BUFCNT \$070	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status; bits for printer hand shake  ;Check transmit register; empty status bit  ;Any transmit delay in progress?; no
000418 000419 000420 000421 000423 000424 000425 000427 000429 000431 000431 000431 000435 000437 000438 000437 000444 000445 000444 000444 000444 000444 000444 000445 000445 000445 000450	;; ; ACIA Maste ; ;	SBC STA BCS DEC JMP .PAGE TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BNE  TYA AND BEQ LDA BEQ DEC JMP  LDA BEQ LDA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010 DLYCNT \$010 DLYCNT \$010 DLYCNT \$070 BUFCNT \$070 BUFHEAD	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress? ; no  ;Any data to transmit?
000418 000419 000420 000421 000423 000425 000426 000427 000428 000431 000431 000433 000433 000434 000435 000437 000438 000441 000441 000441 000445 000445 000445 000445	;; ; ACIA Maste ; ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ LDA BEQ DEC JMP  LDA BEQ LDA BEQ LDA BEQ LDA BEQ LDA BEQ LDA LDA BEQ LDA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010 DLYCNT \$010 BUFCNT \$070 BUFHEAD LOCBUF,X	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress? ; no  ;Any data to transmit? ; no wait for completion
000418 000419 000420 000421 000423 000424 000425 000427 000428 000431 000433 000433 000434 000435 000436 000447 000448 000445 000448 000449 000441 000445 000445 000453	;; ; ACIA Maste ; ;	SBC STA BCS DEC JMP .PAGE TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ LDA BEQ DEC JMP  LDA BEQ LDA BEQ LDA STA  LDA BEQ LDA STA  LDA BEQ LDA STA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010 DLYCNT \$010 DLYCNT \$010 DLYCNT \$070 BUFCNT \$070 BUFHEAD	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress? ; no  ;Any data to transmit?
000418 000419 000420 000421 000423 000425 000426 000427 000428 000431 000431 000433 000433 000434 000435 000437 000438 000441 000441 000441 000445 000445 000445 000445	;; ; ACIA Maste ; ;	SBC STA BCS DEC JMP .PAGE  TINTETRUPT  .EQU LDA ORA STA  TYA AND BNE  TYA AND BEQ LDA BEQ DEC JMP  LDA BEQ LDA BEQ LDA BEQ LDA BEQ LDA BEQ LDA LDA BEQ LDA	MOVCNT REQCNT \$010 REQCNT+1 \$010  Handler  * E_REG #BITON7 E_REG  #60 \$080  #BITON4 \$060  DLYCNT \$010 DLYCNT \$010 BUFCNT \$070 BUFHEAD LOCBUF,X	;Loop back for more  ;Set 1 MHz mode  ;Check DSR and DCD status ; bits for printer hand shake  ;Check transmit register ; empty status bit  ;Any transmit delay in progress? ; no  ;Any data to transmit? ; no wait for completion



		DCC	\$020	
000457 000458		BCC LDX	#0	
000459	\$020	STX	BUFHEAD	;Update buffer index
000460 000461		DEC	BUFCNT	; and count
000462		CMP	#ASC_CR	Check for any delay
000463		BEQ	\$040	
000464 000465		BCS CMP	\$060 #ASC_LF	
000466		BNE	\$030	
000467		LDA	LFDELAY	
000468 000469	\$030	BCS CMP	\$050 #ASC_FF	
000470	<b>4030</b>	BNE	\$060	
000471		LDA	FFDELAY	
000472 000473	\$040	BCS LDA	\$050 CRDELAY	
000474		STA	DLYCNT	
000475	<b>4060</b>	.PAGE LDA	ACIACMD	
000476 000477	\$000	AND	#0E0	;Enable transmit interrupt
000478		ORA	#007	<del>-</del>
000479 000480		STA RTS	ACIACMD	
000481		KID		
000482	\$070	ASL	XMIT	orill man dama
000483 000484		BMI	\$060	;Still not done
000485	\$080	LDA	ACIACMD	
000486 000487		AND ORA	#0E0 #00B	Disable transmit interrupt
000487		STA	#00B ACIACMD	
000489		RTS		
000490 000491	:	.PAGE		
000492	;			
	<pre>; Serial Prir ;</pre>	nter Driver -	- Status Request	
000496 000497	OD OMAM	.EQU	*	
000497	SP_STAT	BIT	OPENFLG	;Serial Printer open?
000499		BMI	\$010	
000500	\$010	JMP SWITCH	NOTOPEN CTLSTAT, 2, STATSW	
000502	V010	DWITCH	CIBDINI, 2, BINIBN	
000503	BADCTL	LDA	#XCTLCODE	;Invalid control code
000505	BADCIL	JSR	SYSERR	/invalid control code
000506				
000507 000508	STATSW	.WORD	STAT00-1	
000509		.WORD	STAT01-1	
000510				
		.WORD	STAT02-1	
000511 000512		.WORD	STAT02-1	
000511 000512 000513	STAT00	.WORD	STAT02-1	;0 NOP
000511 000512	STAT00		STAT02-1	;0 NOP
000511 000512 000513 000514 000515 000516	STAT00 STAT01	RTS LDY	#0	;0 NOP ;1 Status Table
000511 000512 000513 000514 000515 000516		RTS LDY LDA	#0 #0	
000511 000512 000513 000514 000515 000516		RTS LDY	#0	
000511 000512 000513 000514 000515 000516 000517 000518 000519		RTS LDY LDA STA	#0 #0	
000511 000512 000513 000514 000515 000516 000517 000518 000519 000520 000521	STAT01	RTS LDY LDA STA RTS	#0 #0 (CSLIST),Y	;1 Status Table
000511 000512 000513 000514 000515 000516 000517 000518 000519 000520 000521 000522		RTS LDY LDA STA RTS LDY LDA	#0 #0 (CSLIST),Y #0 #FALSE	
000511 000512 000513 000514 000515 000516 000517 000519 000520 000521 000522 000523 000524	STAT01	RTS LDY LDA STA RTS LDY LDA STA	#0 #0 (CSLIST),Y	;1 Status Table
000511 000512 000513 000514 000515 000516 000517 000518 000519 000520 000521 000522	STAT01	RTS LDY LDA STA RTS LDY LDA	#0 #0 (CSLIST),Y #0 #FALSE	;1 Status Table
000511 000512 000513 000514 000515 000516 000517 000518 000520 000521 000522 000523 000524 000526 000526	STAT01 STAT02	RTS  LDY LDA STA RTS  LDY LDA STA RTS  LDY LDA STA RTS .PAGE	#0 #0 (CSLIST),Y #0 #FALSE	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000527 000528	STAT01  STAT02	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE	#0 #0 (CSLIST),Y #0 #FALSE (CSLIST),Y	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000527 000528 000528 000529	STAT01  STAT02  ;; ; Serial Prir;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000527 000528 000528 000529	STAT01  STAT02  ;; ; Serial Prir;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE	#0 #0 (CSLIST),Y #0 #FALSE (CSLIST),Y	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000526 000527 000528 000529 000530 000531	STAT01  STAT02  ;; ; Serial Prir;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .page	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request	;1 Status Table ;2 New Line
000511 000513 000513 000514 000515 000516 000517 000520 000521 000523 000524 000525 000526 000527 000528 000529 000530 000531 000533	STAT01  STAT02  ;; ; Serial Prir; ; ;	RTS  LDY LDA STA RTS  LDY LDA STA RTS .PAGE  atter Driver -	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000526 000527 000528 000529 000530 000531	STAT01  STAT02  ;; ; Serial Prir; ; ;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .page	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request	;1 Status Table ;2 New Line
000511 000513 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000527 000528 000529 000530 000531 000533 000534 000535	STAT01  STAT02  ;; ; Serial Prir; ; ;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .nter DriverEQU BIT BMI JMP SWITCH	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000525 000526 000527 000528 000529 000531 000532 000533	STAT01  STAT02  ;; ; Serial Prir; ;;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .TEQU BIT BMI JMP	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000524 000526 000527 000526 000527 000530 000531 000533 000534 000535	STAT01  STAT02  ;; ; Serial Prir; ;;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  PAGE  DEPTORMENT  SWITCH JMP .WORD	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000529 000520 000523 000523 000525 000526 000527 000528 000529 000531 000531 000532 000533 000534 000533	STAT01  STAT02  ;; ; Serial Prir; ;;	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .TECU BIT BMI JMP SWITCH JMP .WORD .WORD	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1 CNTL01-1	;1 Status Table ;2 New Line
000511 000512 000513 000514 000515 000516 000517 000518 000520 000521 000522 000523 000524 000526 000527 000528 000529 000531 000531 000531 000536 000537 000538	STAT01  STAT02  ; ; Serial Prir;; ; SP_CNTL  \$010  CNTLSW	RTS  LDY LDA STA RTS  LDY LDA STA RTS  PAGE  THERMIT BMI JMP SWITCH JMP  .WORD .WORD .WORD	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1 CNTL01-1 CNTL02-1	;1 Status Table  ;2 New Line  ;Serial Printer open?; Ok
000511 000512 000513 000514 000515 000516 000517 000520 000521 000522 000523 000525 000526 000527 000528 000529 000531 000531 000532 000533 000534 000539	STAT01  STAT02  ; ; Serial Print; ; SP_CNTL  \$010  CNTLSW	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .PAGE  .EQU BIT BMI JMP SWITCH JMP .WORD .WORD .WORD .WORD	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1 CNTL01-1 CNTL02-1 *	;1 Status Table  ;2 New Line  ;Serial Printer open? ; Ok
000511 000512 000513 000514 000515 000516 000517 000518 000520 000521 000522 000523 000524 000526 000527 000528 000529 000531 000531 000531 000536 000537 000538	STAT01  STAT02  ; ; Serial Print; ; SP_CNTL  \$010  CNTLSW	RTS  LDY LDA STA RTS  LDY LDA STA RTS  PAGE  THERMIT BMI JMP SWITCH JMP  .WORD .WORD .WORD	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1 CNTL01-1 CNTL02-1	;1 Status Table  ;2 New Line  ;Serial Printer open?; Ok
000511 000512 000513 000514 000515 000516 000517 000529 000521 000522 000523 000525 000526 000527 000528 000529 000531 000531 000532 000533 000534 000534 000542	STAT01  STAT02  ; ; Serial Print; ; SP_CNTL  \$010  CNTLSW	RTS  LDY LDA STA RTS  LDY LDA STA RTS  .PAGE  .PAGE  .EQU BIT BMI JMP SWITCH JMP .WORD .WORD .WORD .WORD .EQU BIT BMI LDA	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1 CNTL01-1 CNTL02-1  * XMIT \$010 #00	;1 Status Table  ;2 New Line  ;Serial Printer open? ; Ok
000511 000512 000513 000514 000515 000516 000517 000518 000520 000521 000522 000523 000524 000525 000526 000527 000528 000531 000533 000534 000535 000536 000537	STAT01  STAT02  ; ; Serial Print; ; SP_CNTL  \$010  CNTLSW	RTS  LDY LDA STA RTS  LDY LDA STA RTS  PAGE  THER Driver -  EQU BIT BMI JMP SWITCH JMP .WORD .WORD .WORD .WORD .EQU BIT BMI BMI JMP SWITCH JMP	#0 #0 (CSLIST),Y  #0 #FALSE (CSLIST),Y  - Control Request  * OPENFLG \$010 NOTOPEN CTLSTAT,2,CNTLSW BADCTL CNTL00-1 CNTL01-1 CNTL02-1  * XMIT \$010	;1 Status Table  ;2 New Line  ;Serial Printer open? ; Ok



```
000550
                    PHP
000551
000552
                    SEI
LDA
                                E_REG
000553
                    TAX
000554
                    ORA
                                #BITON7
000555
                     STA
                                E_REG
                                                       ;Switch to 1 MHz
000556
                     STA
                                ACIASTAT
                                                       ;Reset ACIA
                                DFORMAT
000557
                    T<sub>1</sub>DA
000558
                    AND
                                #0F0
000559
000560
000561
                    ORA
                                DRATE
                                ACIACTL
DFORMAT
                                                       ;Set up ACIA control register
                    STA
                    LDA
000562
000563
                    ASL
                                Α
                    ASL
                                Α
000564
                    ASL
000565
000566
                    ASL
                                Α
                    ORA
                                #00B
000567
                     STA
                                ACIACMD
                                                       ;Set up ACIA command register
                                                       ;Switch back to 2 MHz
000568
000569
                    STX
                                E_REG
                    PLP
000570
000571
000572 CNTL01
                     . EQU
                                                       ;1 -- Serial Printer Status Table
000573
000574
000575
      CNTL02
                     . EQU
                                                       ;2 -- New Line
000576
                    RTŜ
000577
                    .END
000578
END OF FILE: SERPRINT.text
LINES: 578
CHARACTERS: 26582
```

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